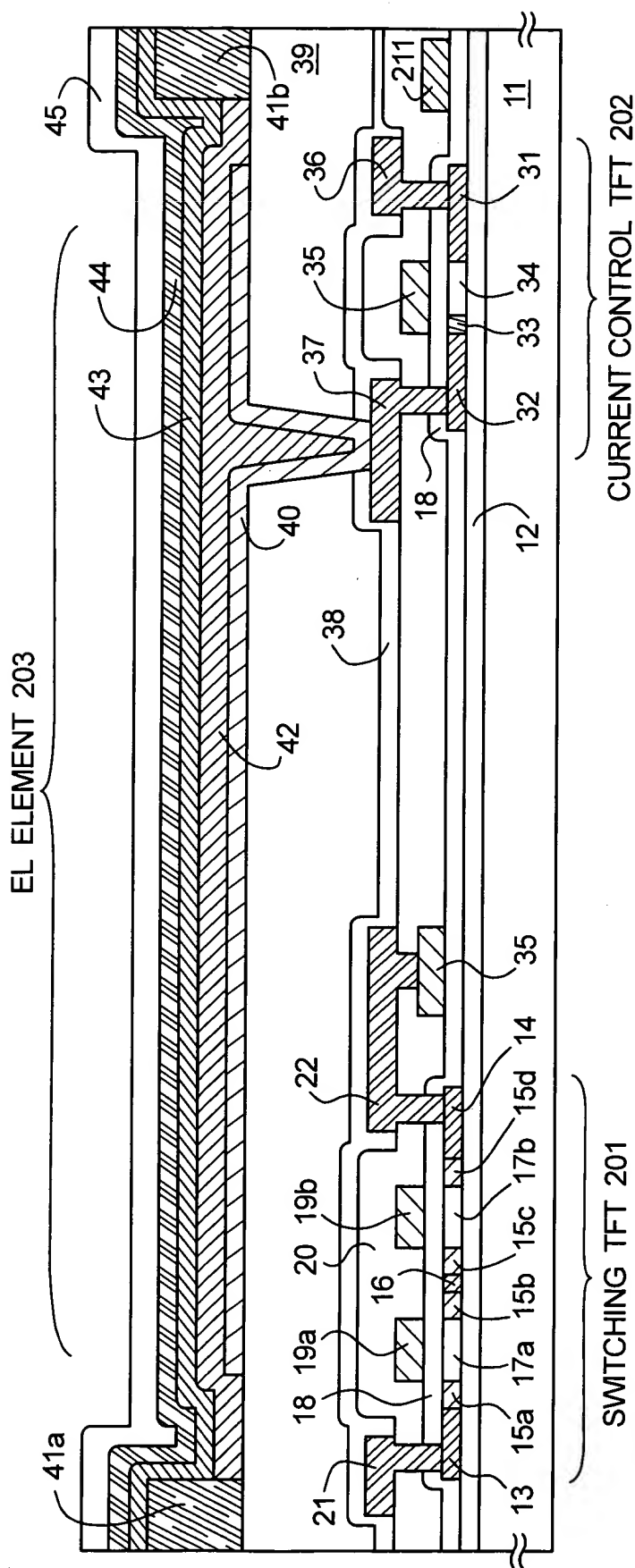


**FIG. 1C**



- 11:SUBSTRATE 12:BASE FILM 13:SOURCE REGION 14:DRAIN REGION 15a-15d:LDD REGIONS  
 16:HIGH CONCENTRATION IMPURITY REGIONS 7a,17b:CHANNEL FORMING REGION  
 18:GATE INSULATING FILM 19a,19b:GATE ELECTRODES 20:FIRST INTERLAYER INSULATING FILM  
 21:SOURCE WIRING 22:DRAIN WIRING 23:GATE ELECTRODE 31:SOURCE REGION 32:DRAIN REGION  
 33:LDD REGION 34:CHANNEL FORMING REGION 35:GATE ELECTRODE 36:SOURCE WIRING  
 37:DRAIN WIRING 38:FIRST PASSIVATION FILM 39:SECOND INTERLAYER INSULATING FILM  
 40:PIXEL ELECTRODE(CATHODE) 41:BANK 42:LIGHT EMITTING LAYER 43:HOLE INJECTION LAYER  
 44:ANODE 45:SECOND INTERLAYER INSULATING FILM

FIG. 2

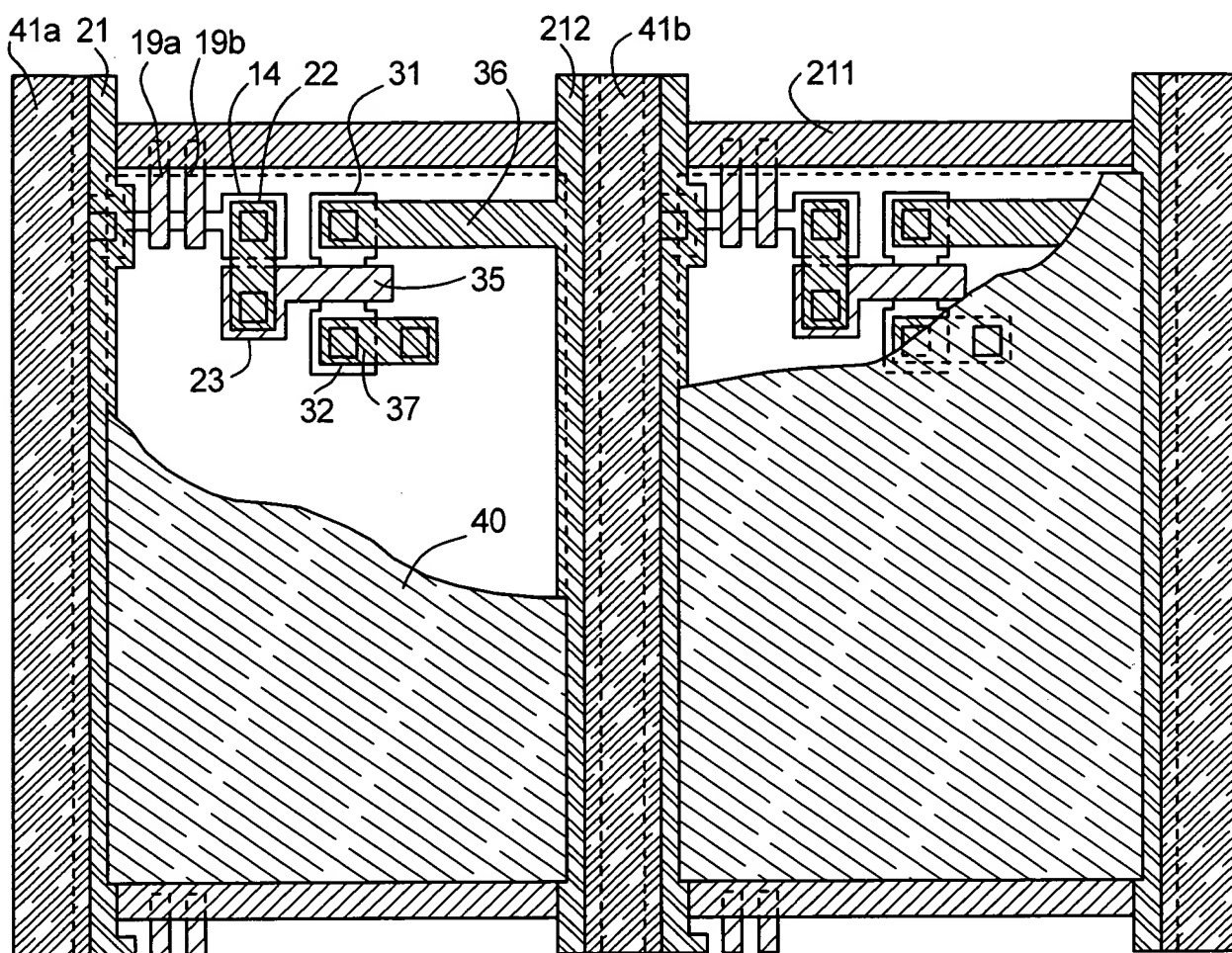


FIG. 3A

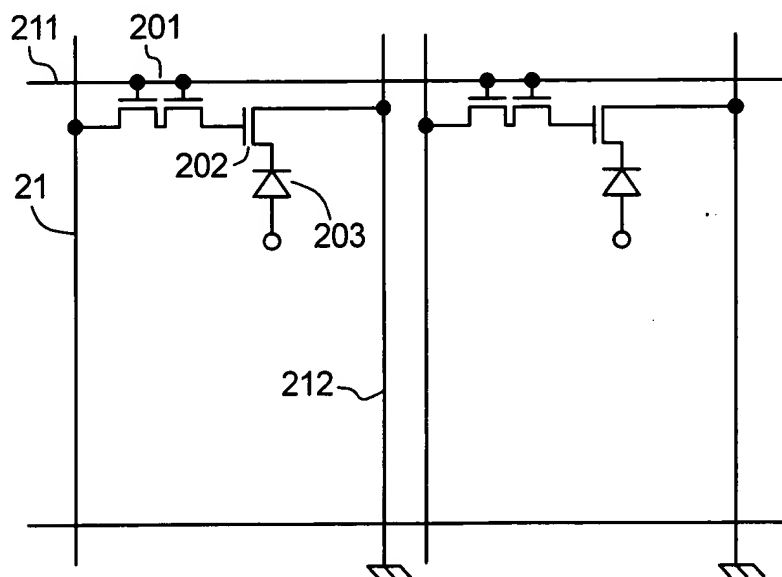


FIG. 3B



FIG. 4A

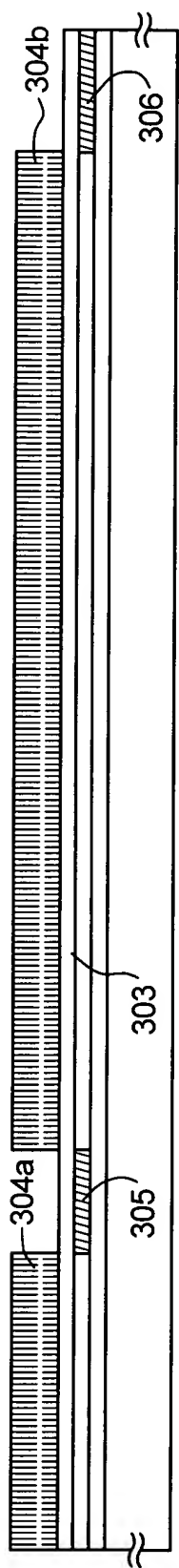


FIG. 4B



FIG. 4C



FIG. 4D

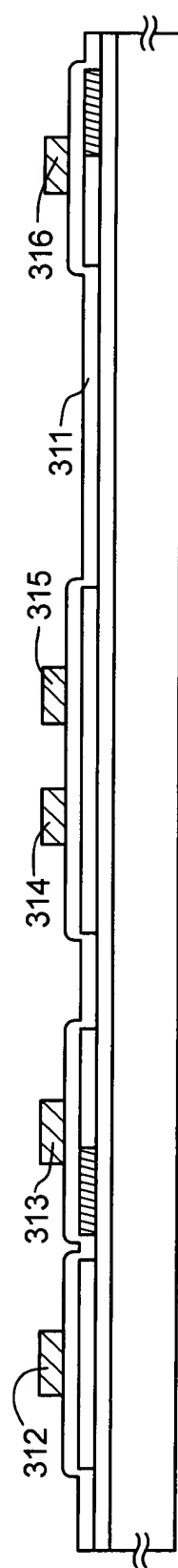


FIG. 4E

300: GLASS SUBSTRATE 301: BASE FILM 302: POLYSILICON FILM 303: PROTECTIVE FILM 304a-304b: RESIST MASK  
305, 306: N-TYPE IMPURITY REGIONS 307-310: ACTIVE LAYERS 311: GATE INSULATING FILM 312-316: GATE ELECTRODES

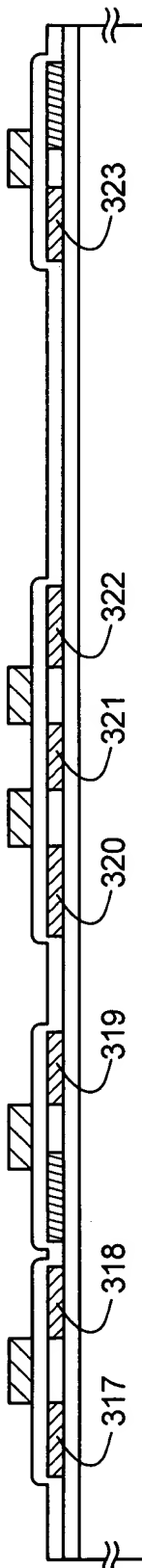


FIG. 5A

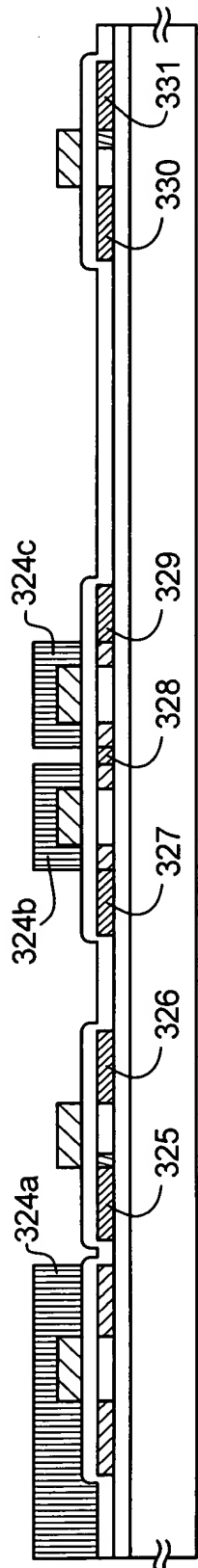


FIG. 5B

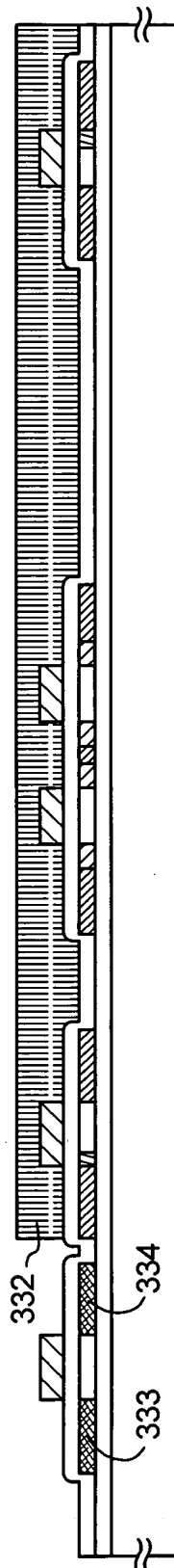


FIG. 5C

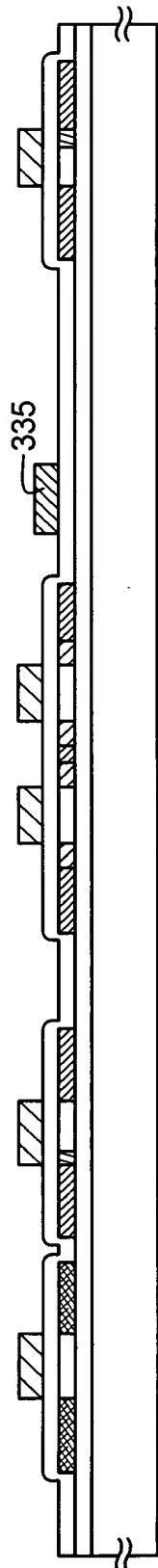
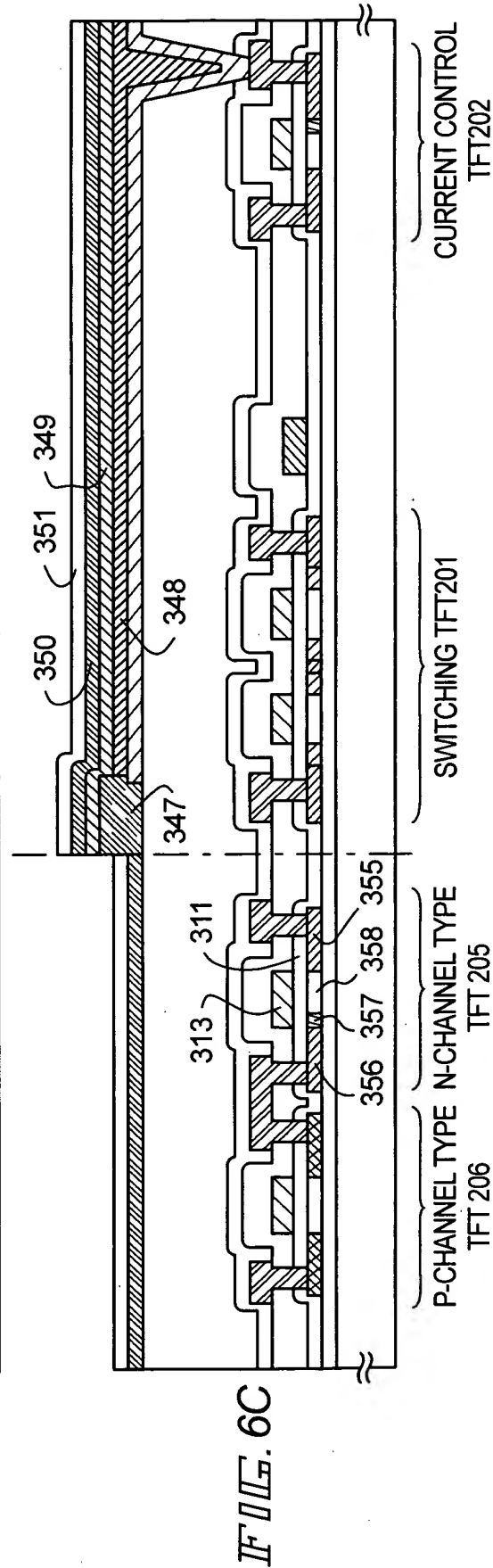
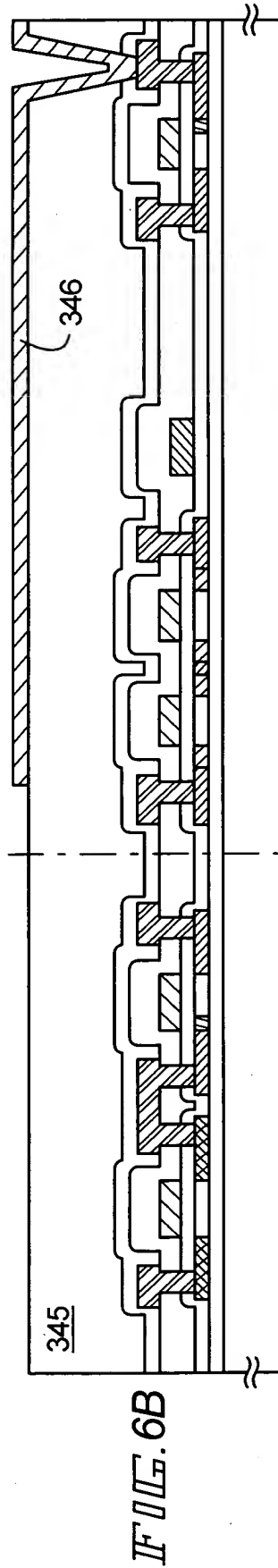
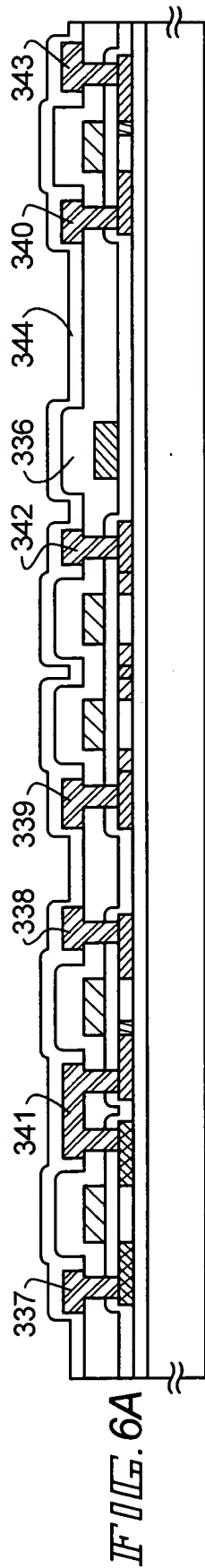
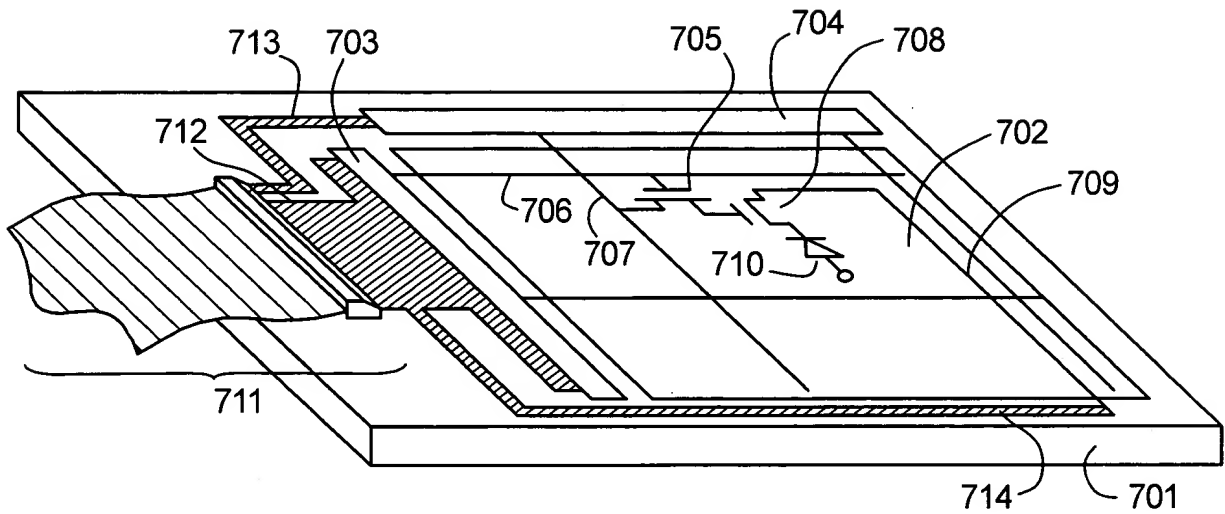


FIG. 5D

317-323: N-TYPE IMPURITY REGIONS 324a-324c: RESIST MASK 325-331: N-TYPE IMPURITY REGIONS  
333, 334: P-TYPE IMPURITY REGIONS 335: GATE WIRING



336: FIRST INTERLAYER INSULATING FILM 337-340: SOURCE WIRING 341-343: DRAIN WIRING  
 344: FIRST PASSIVATION FILM 345: SECOND INTERLAYER INSULATING FILM 346: PIXEL ELECTRODE (CATHODE)  
 347: BANK 348: LIGHT EMITTING LAYER 349: HOLE INJECTION LAYER 350: ANODE 351: SECOND PASSIVATION FILM



701:SUBSTRATE 702:PIXEL PORTION 703:GATE SIDE DRIVER CIRCUIT  
 704:SOURCE SIDE DRIVER CIRCUIT 705:SWITCHING TFT 706:GATE WIRING  
 707:SOURCE WIRING 708:CURRENT CONTROL TFT 709:CURRENT SUPPLY LINE  
 710:EL ELEMENT 711:FPC 712-714:CONNECTION WIRING

FIG. 7

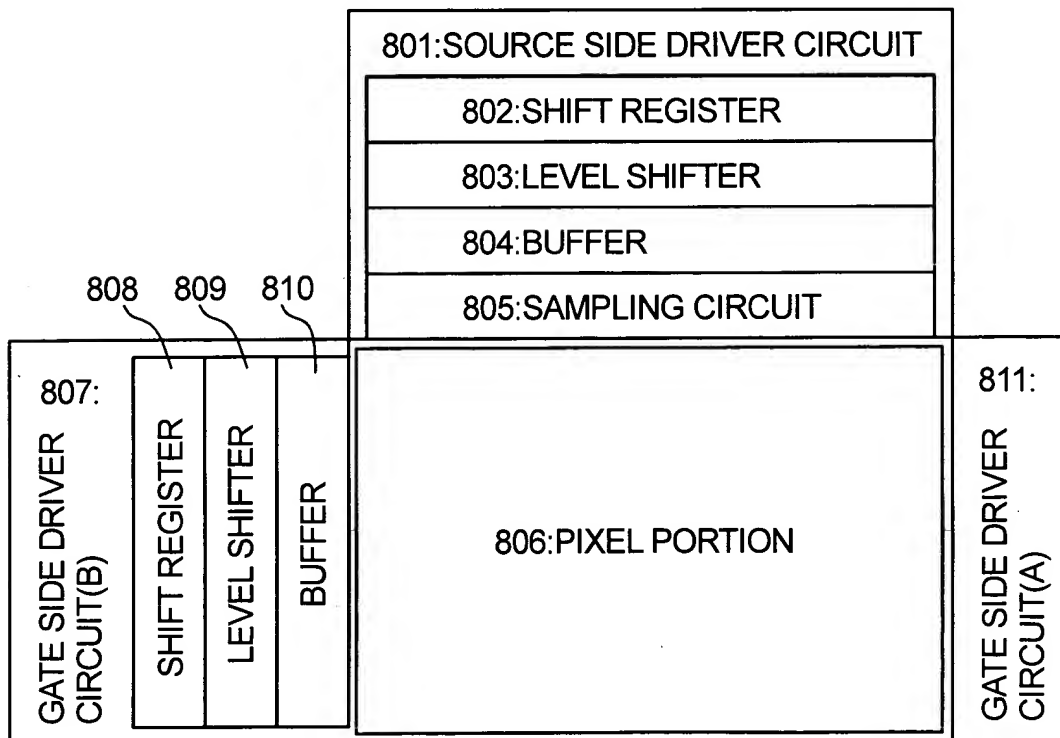


FIG. 8

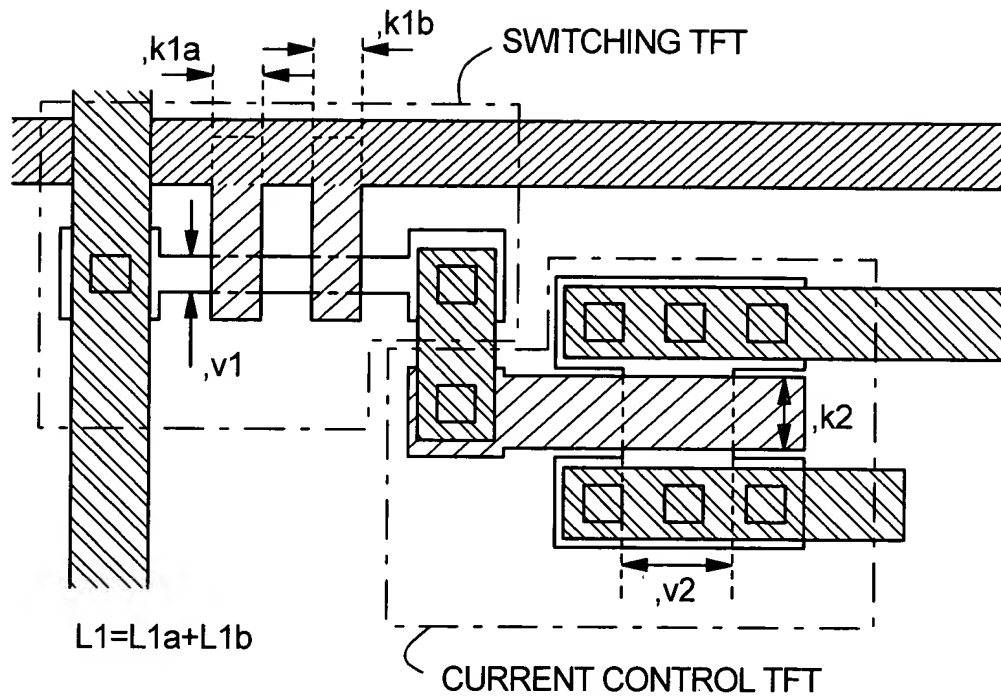


FIG. 9

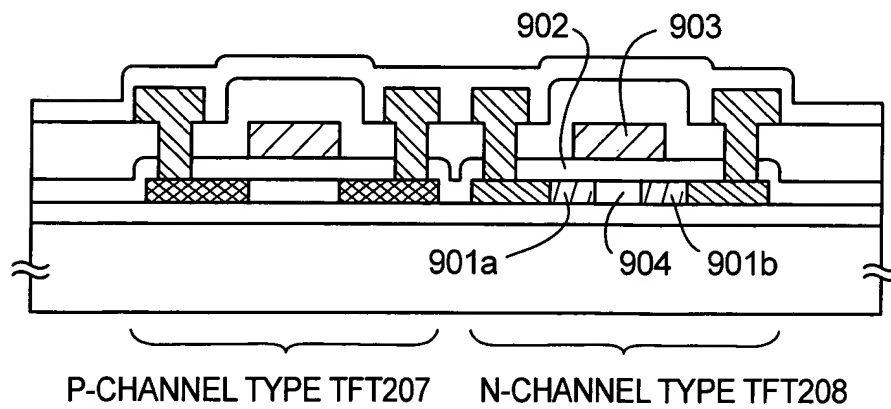
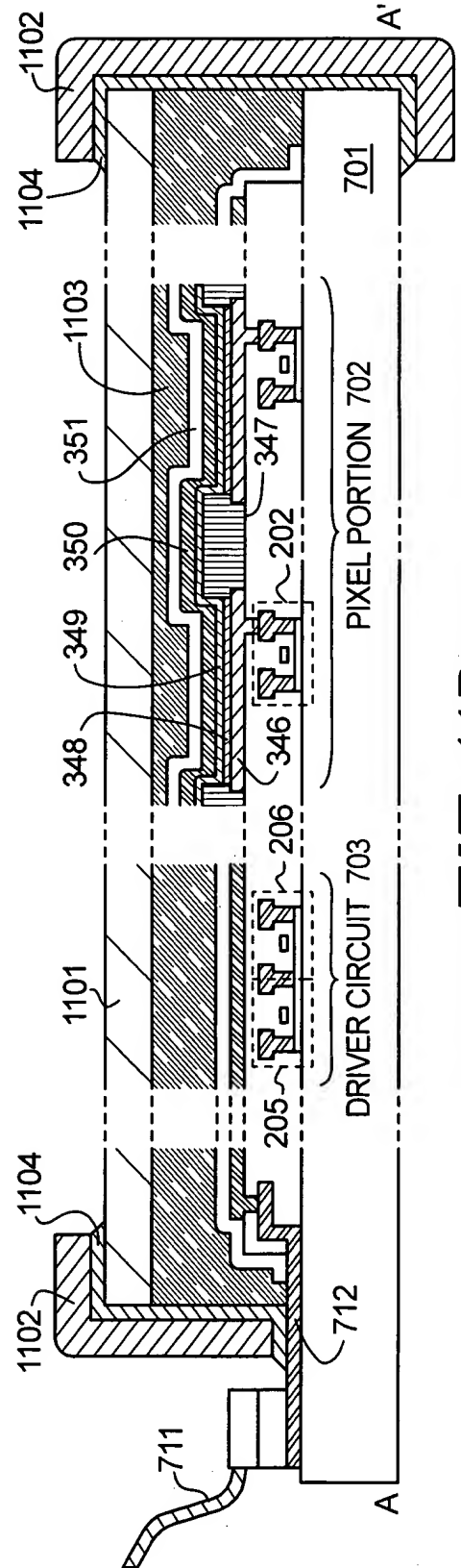
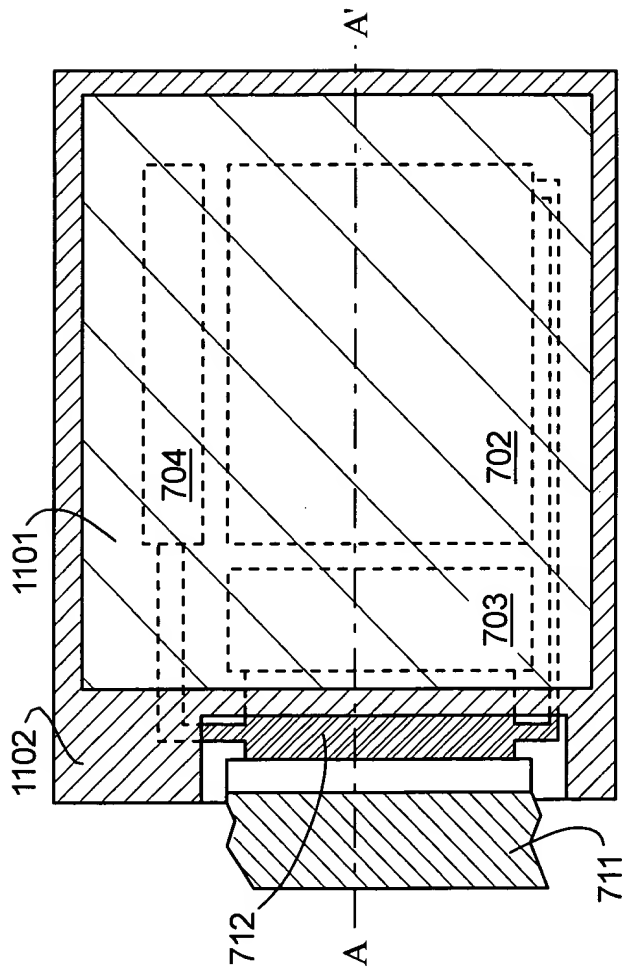


FIG. 10





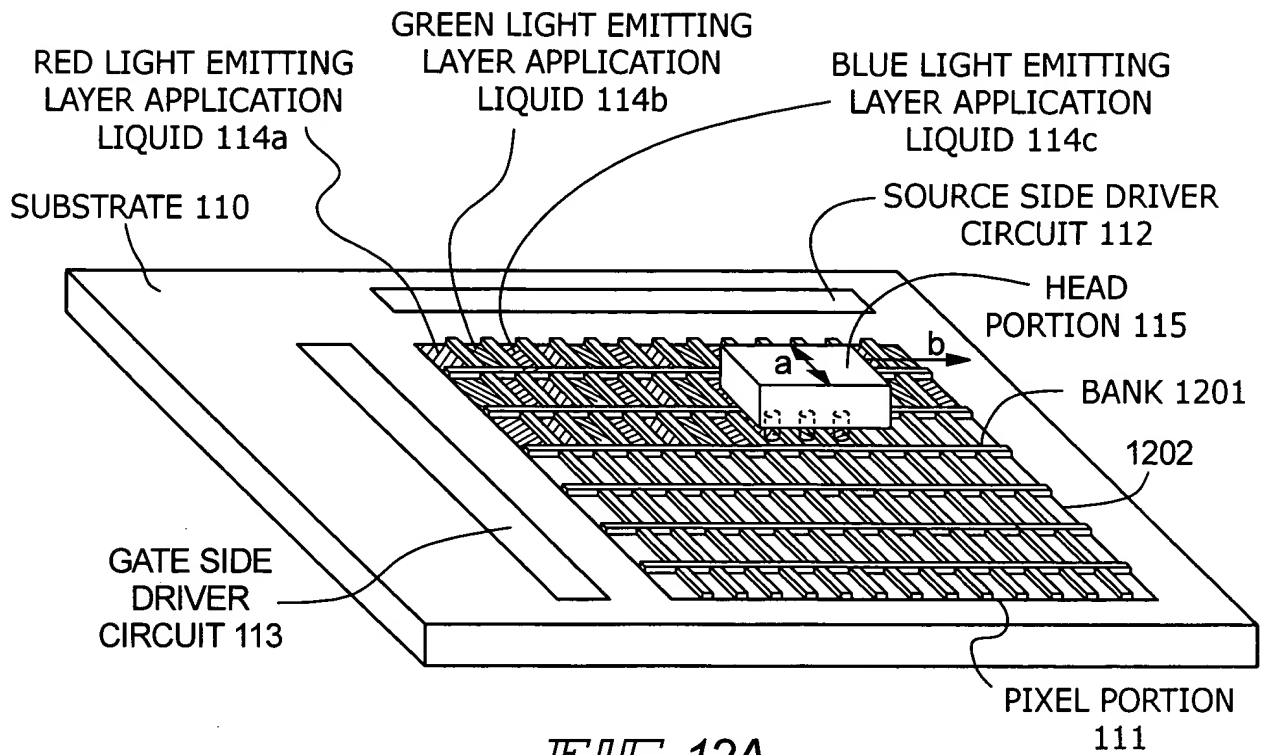


FIG. 12A

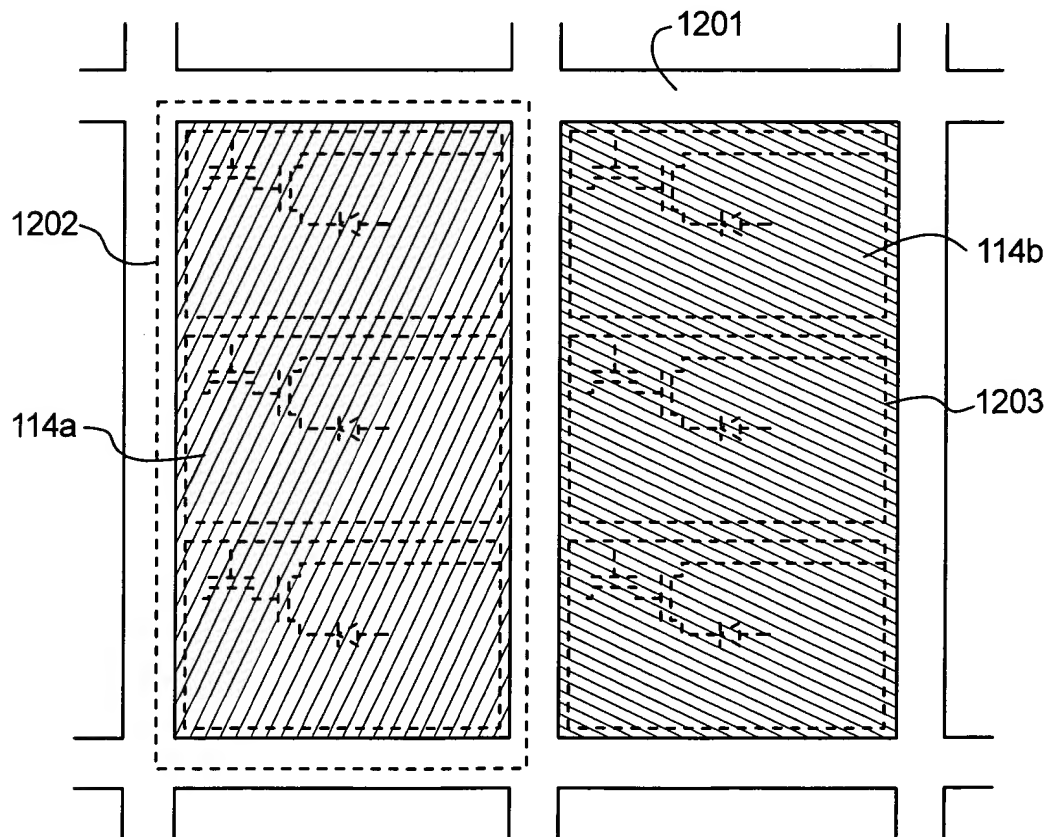


FIG. 12B

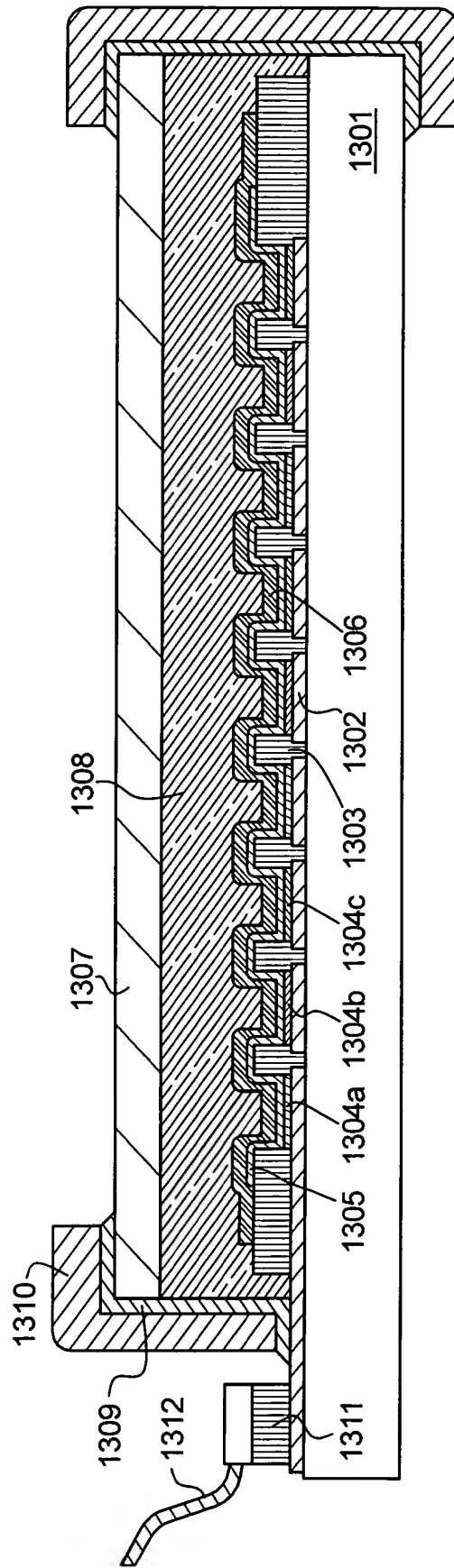


FIG. 13

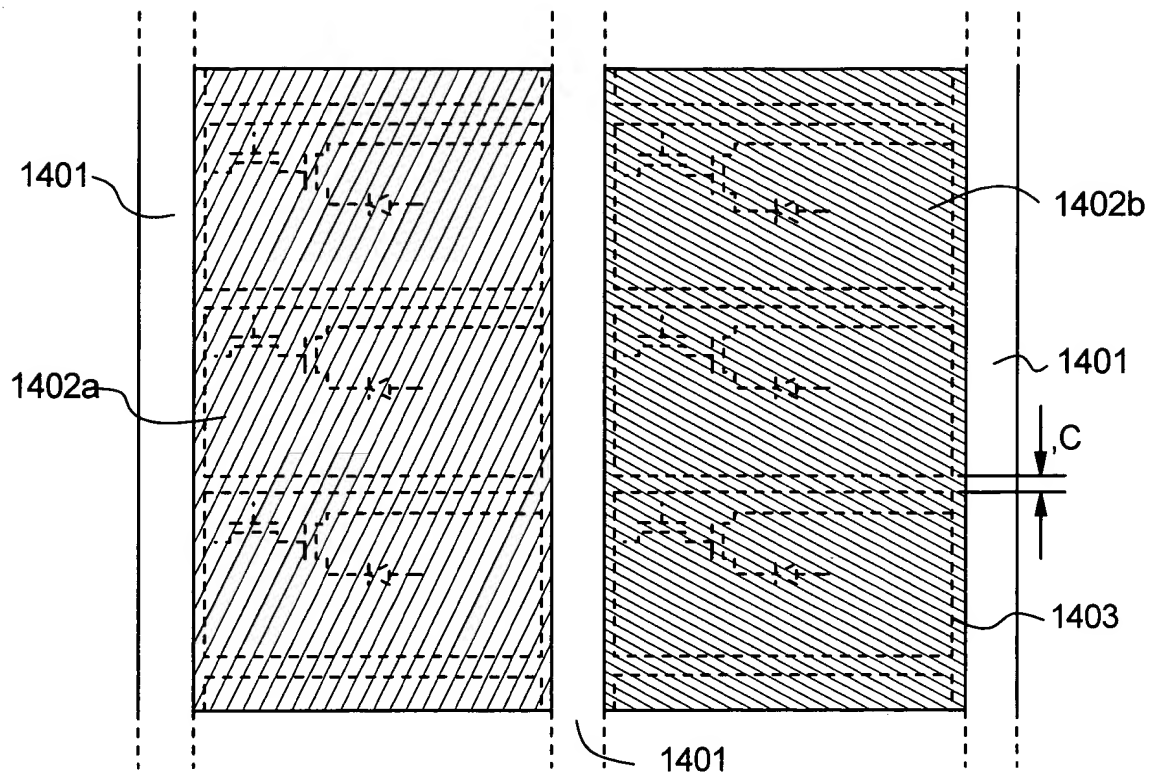


FIG. 14A

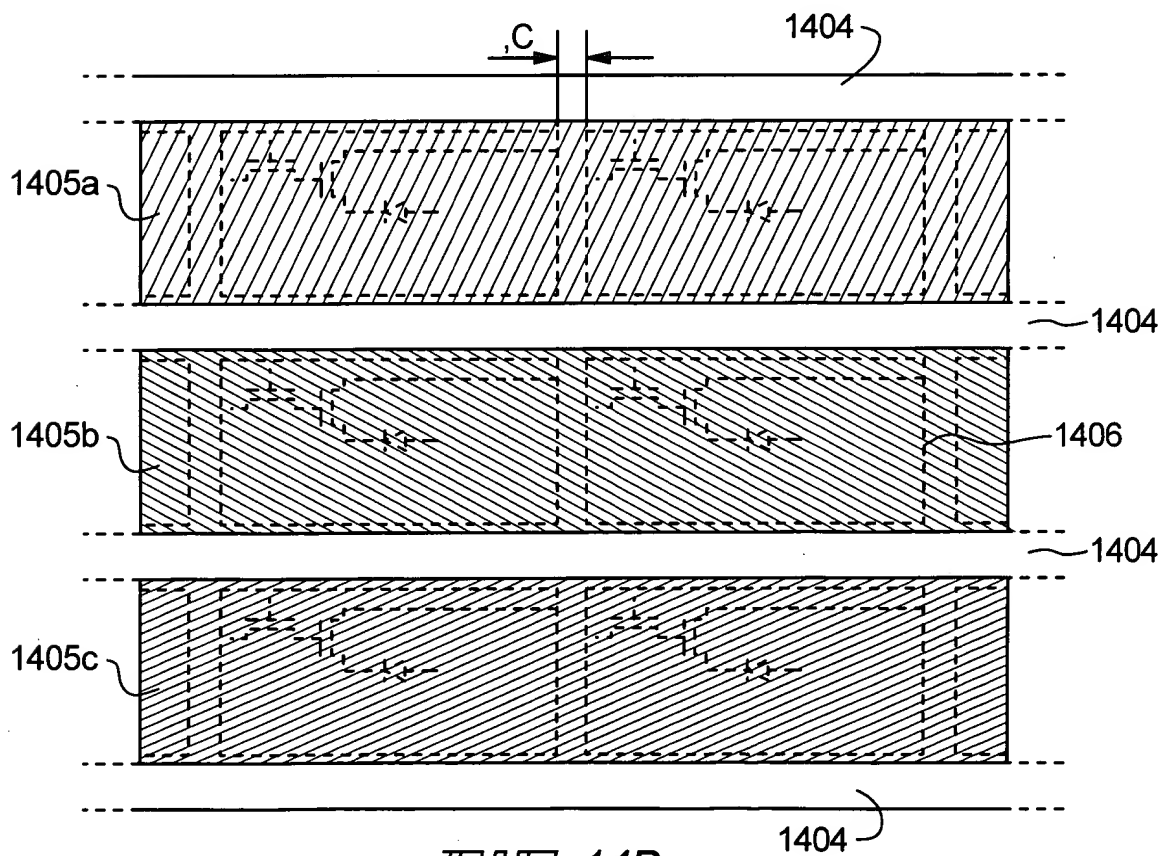


FIG. 14B

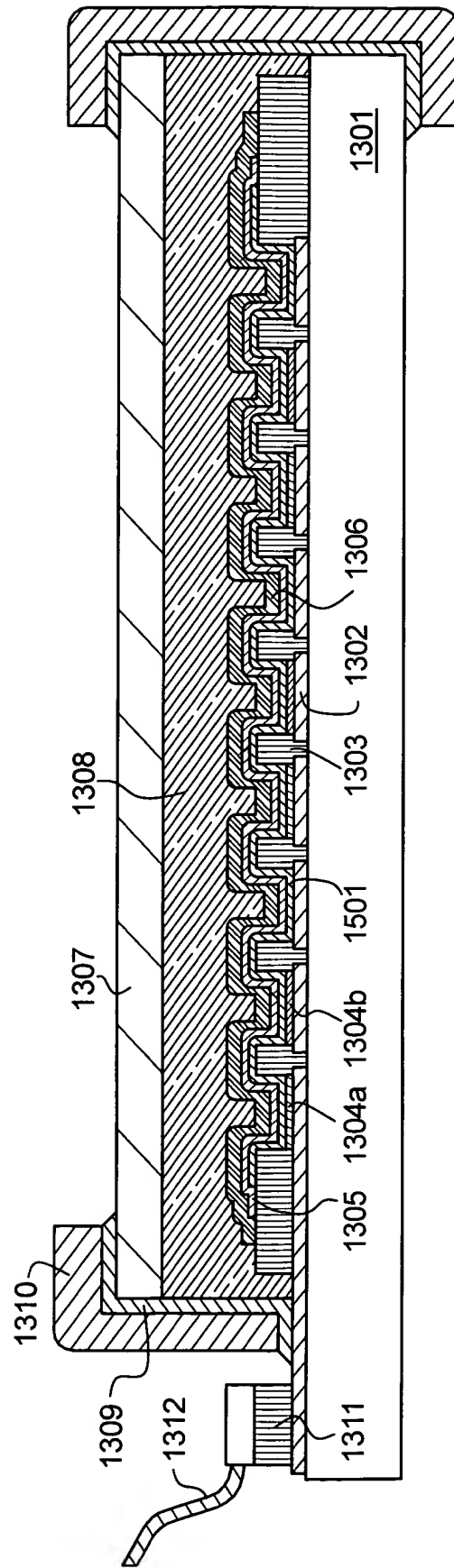
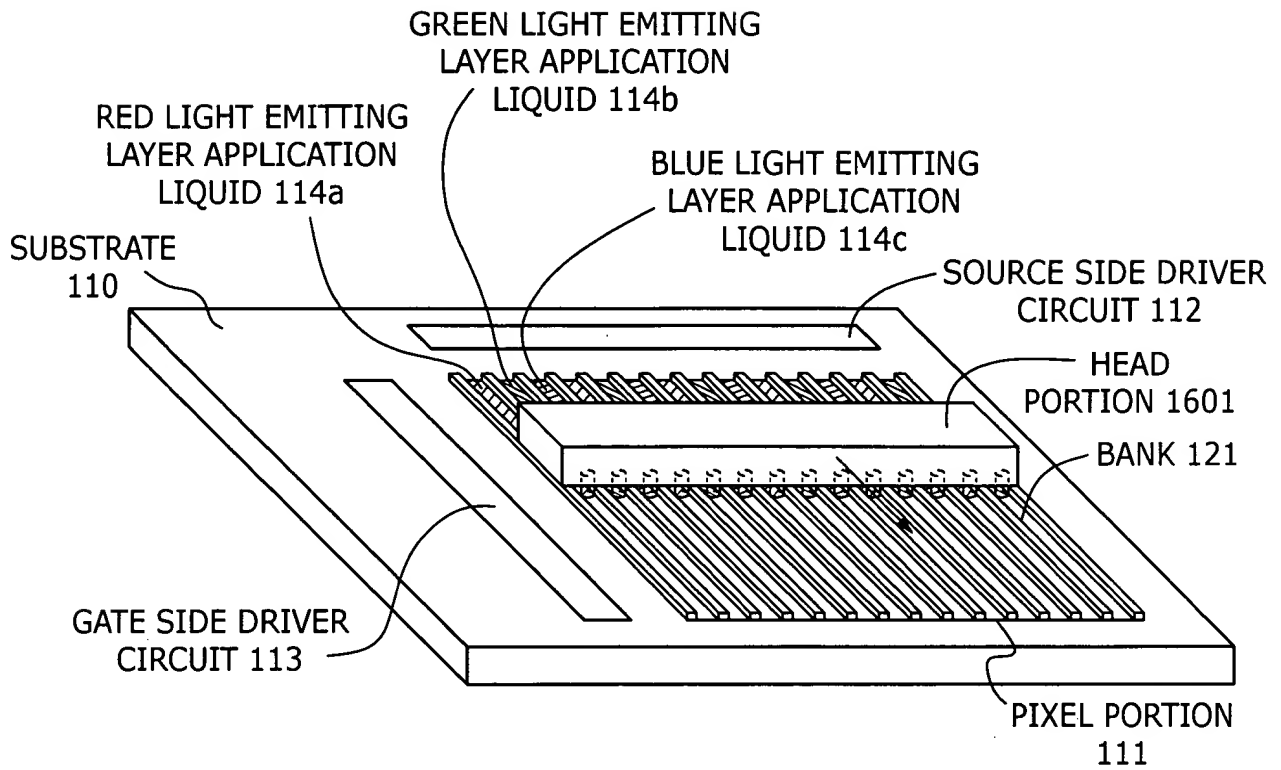
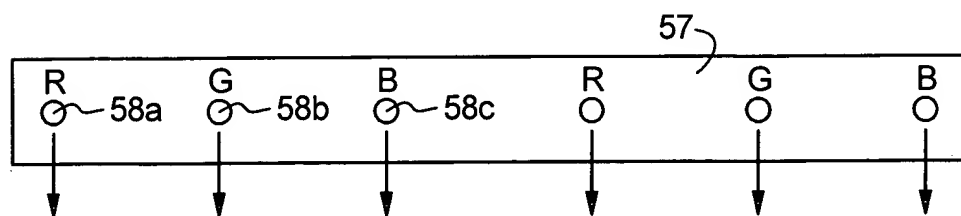
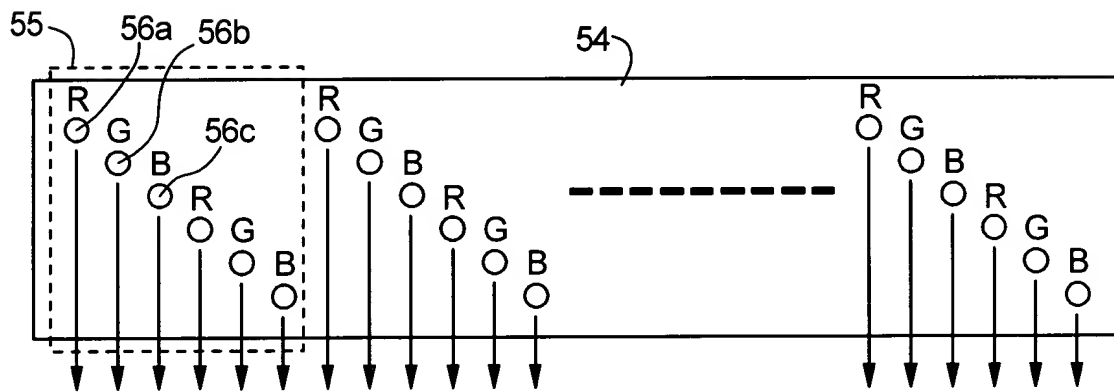
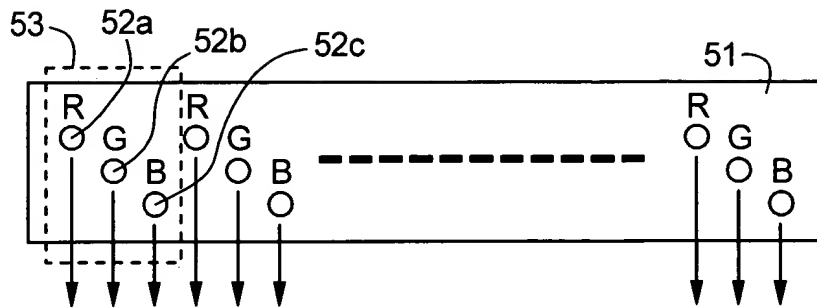


FIG. 15

*FIG. 16*



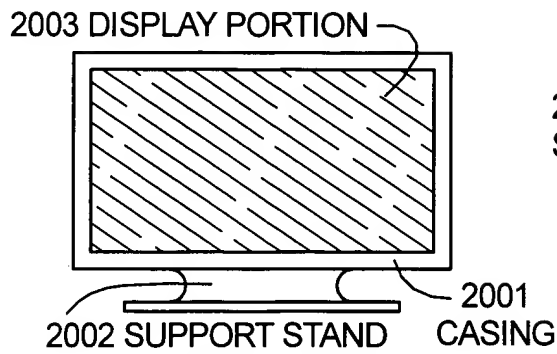


FIG. 18A

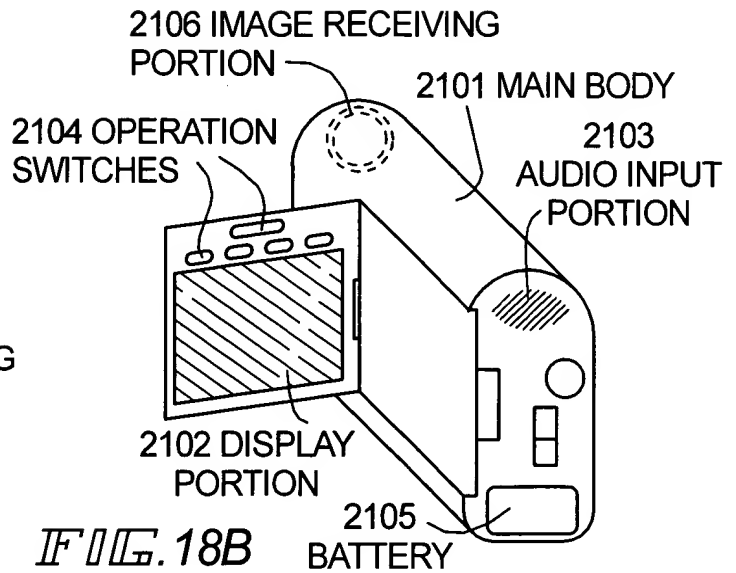


FIG. 18B

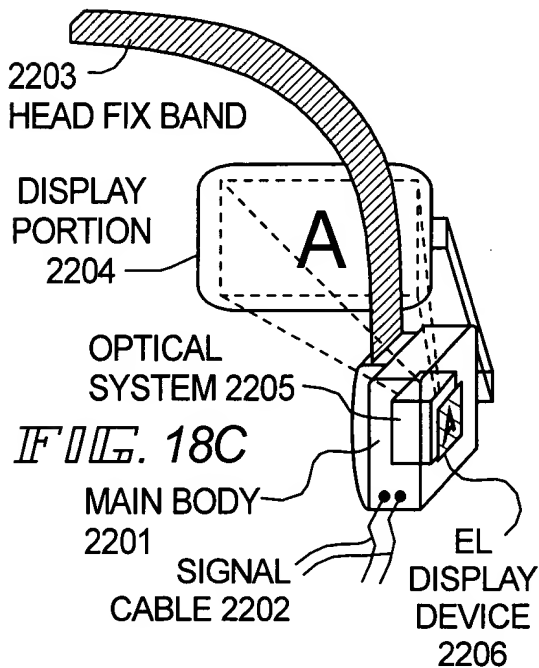


FIG. 18C

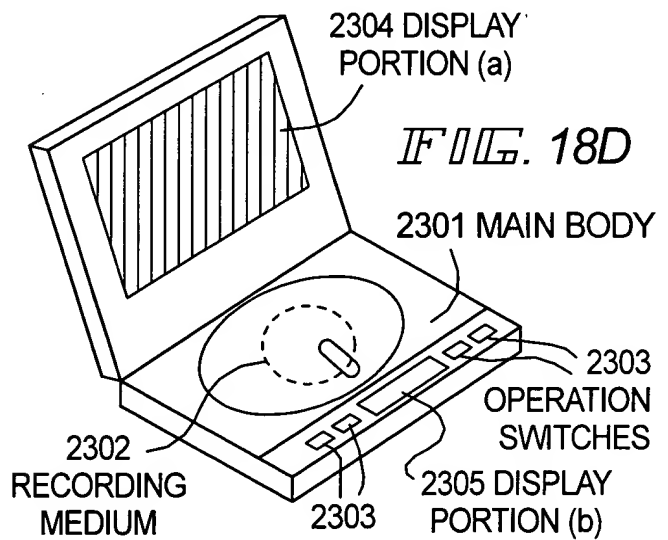


FIG. 18D

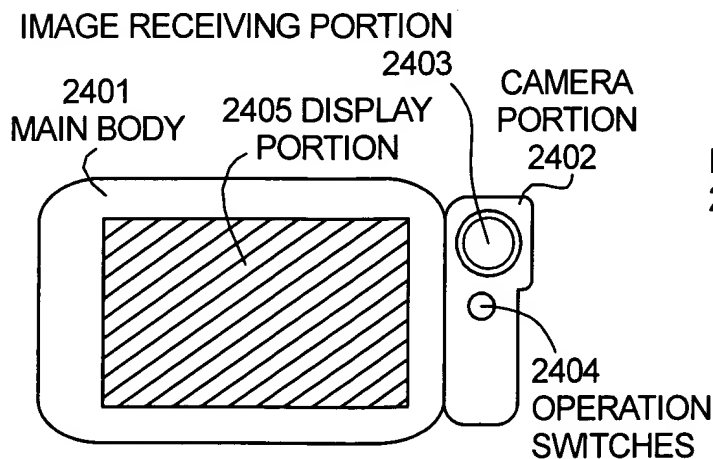


FIG. 18E

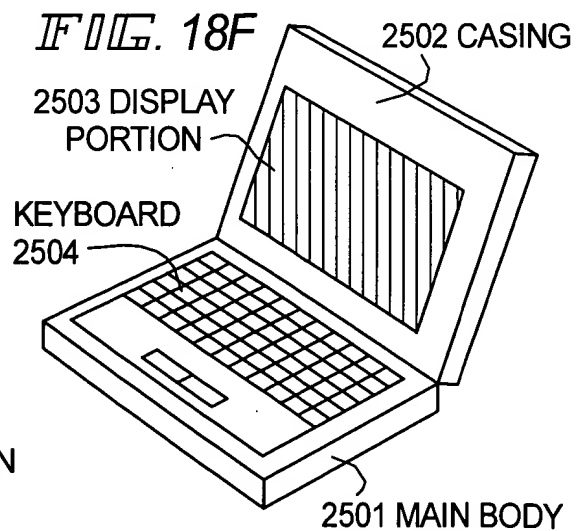
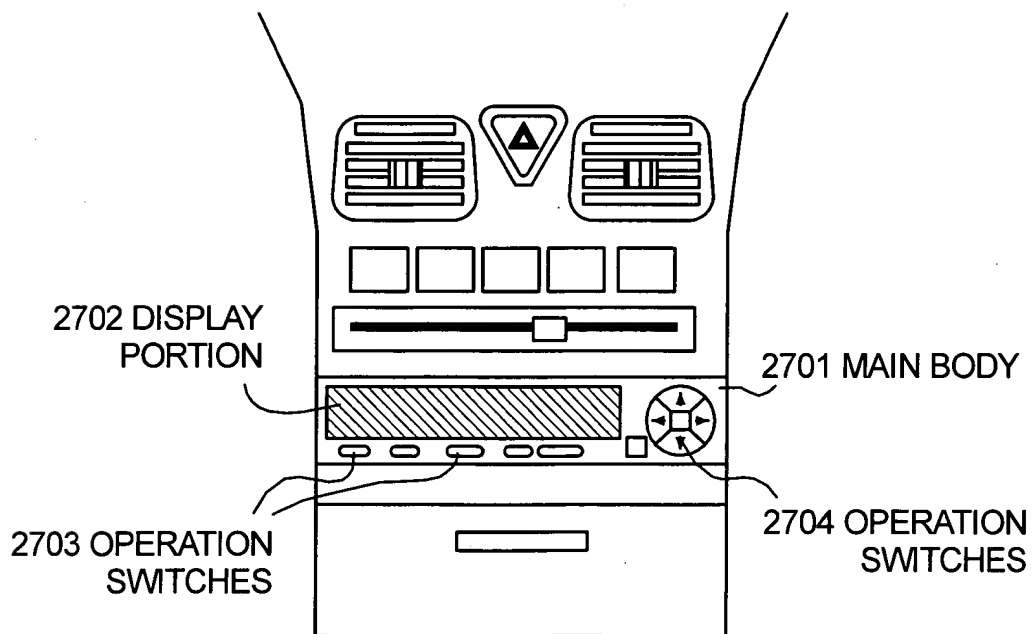
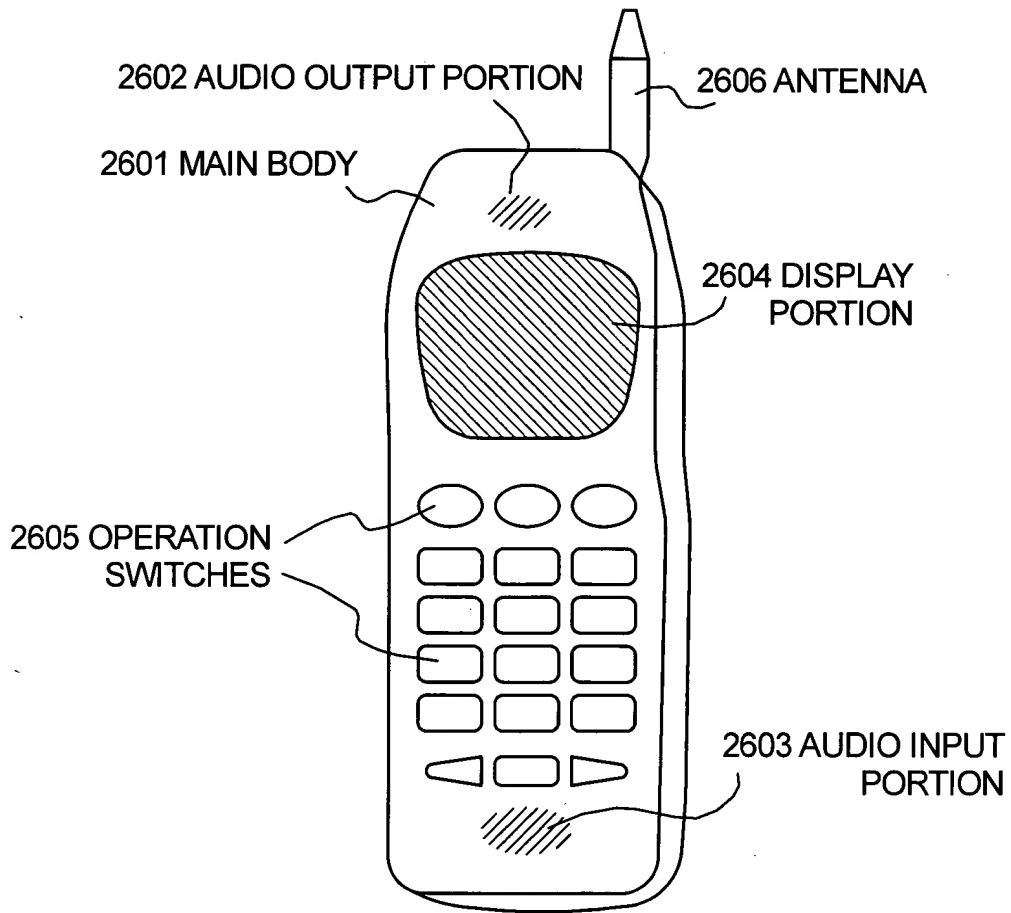


FIG. 18F





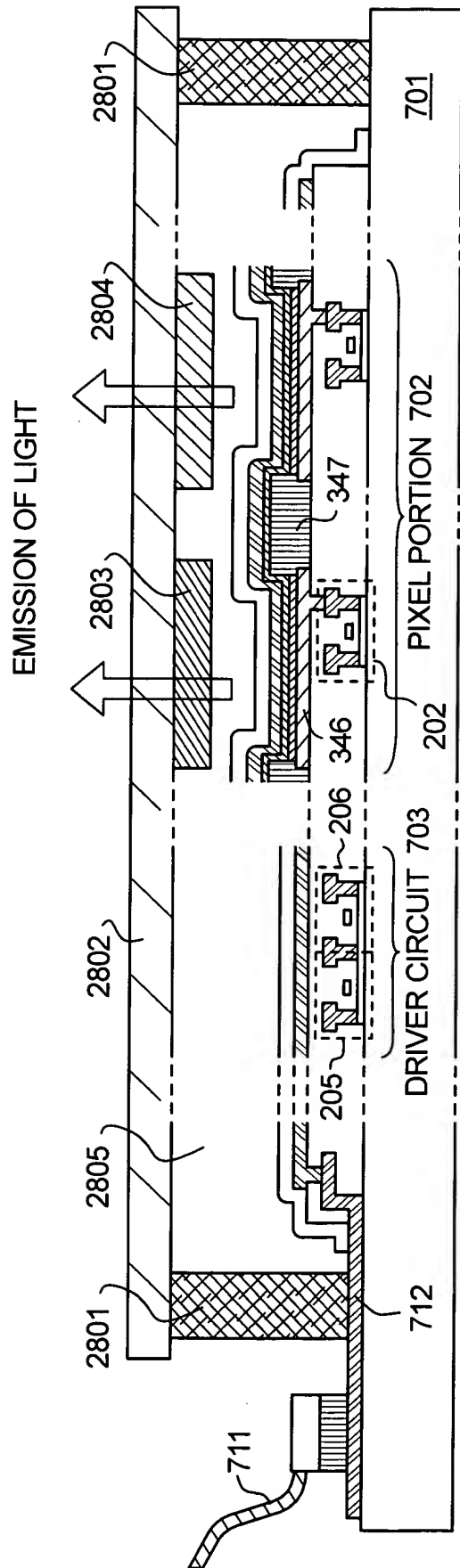
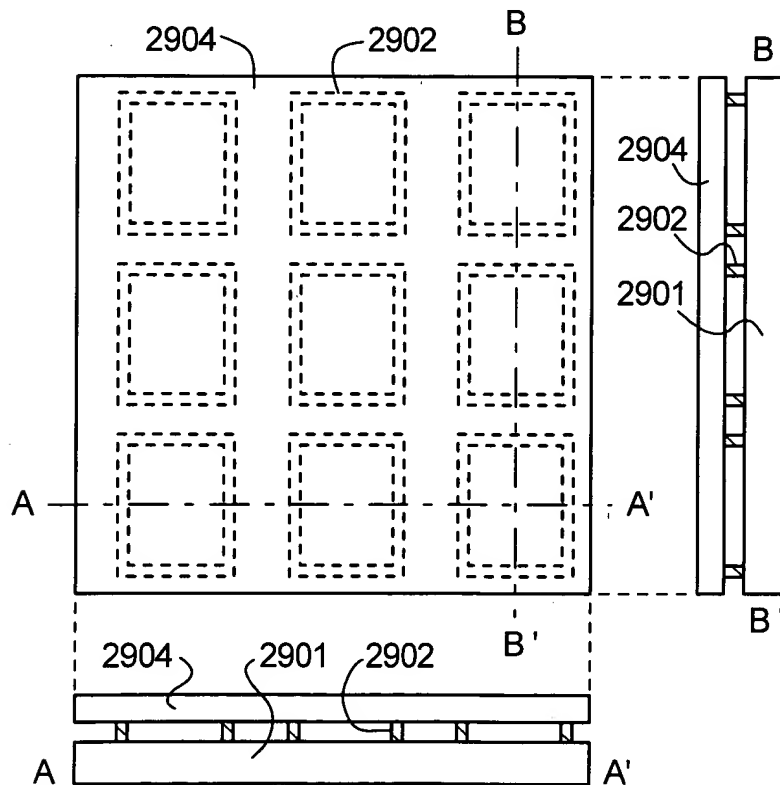
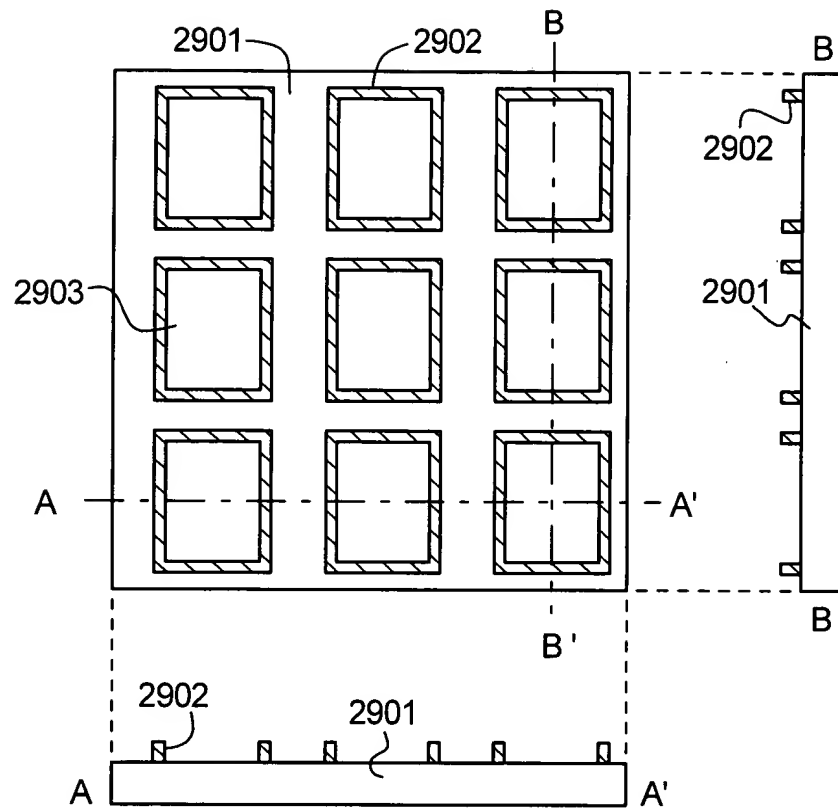


FIG. 20



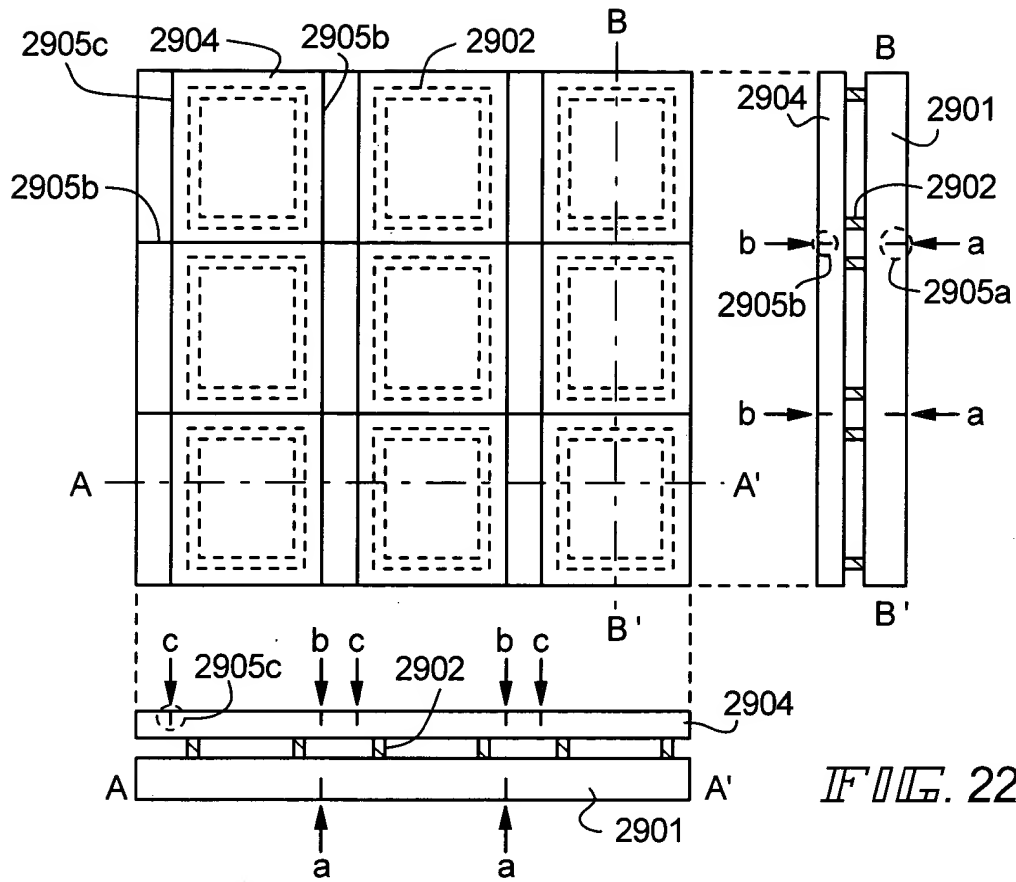


FIG. 22A

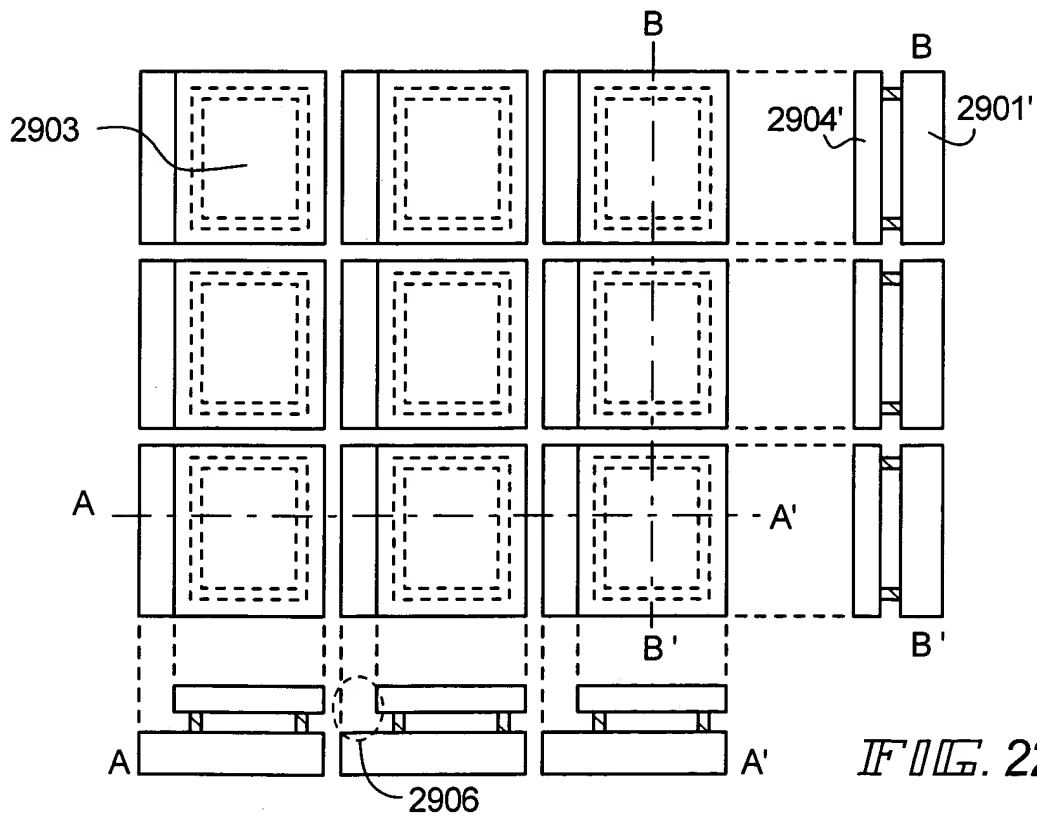
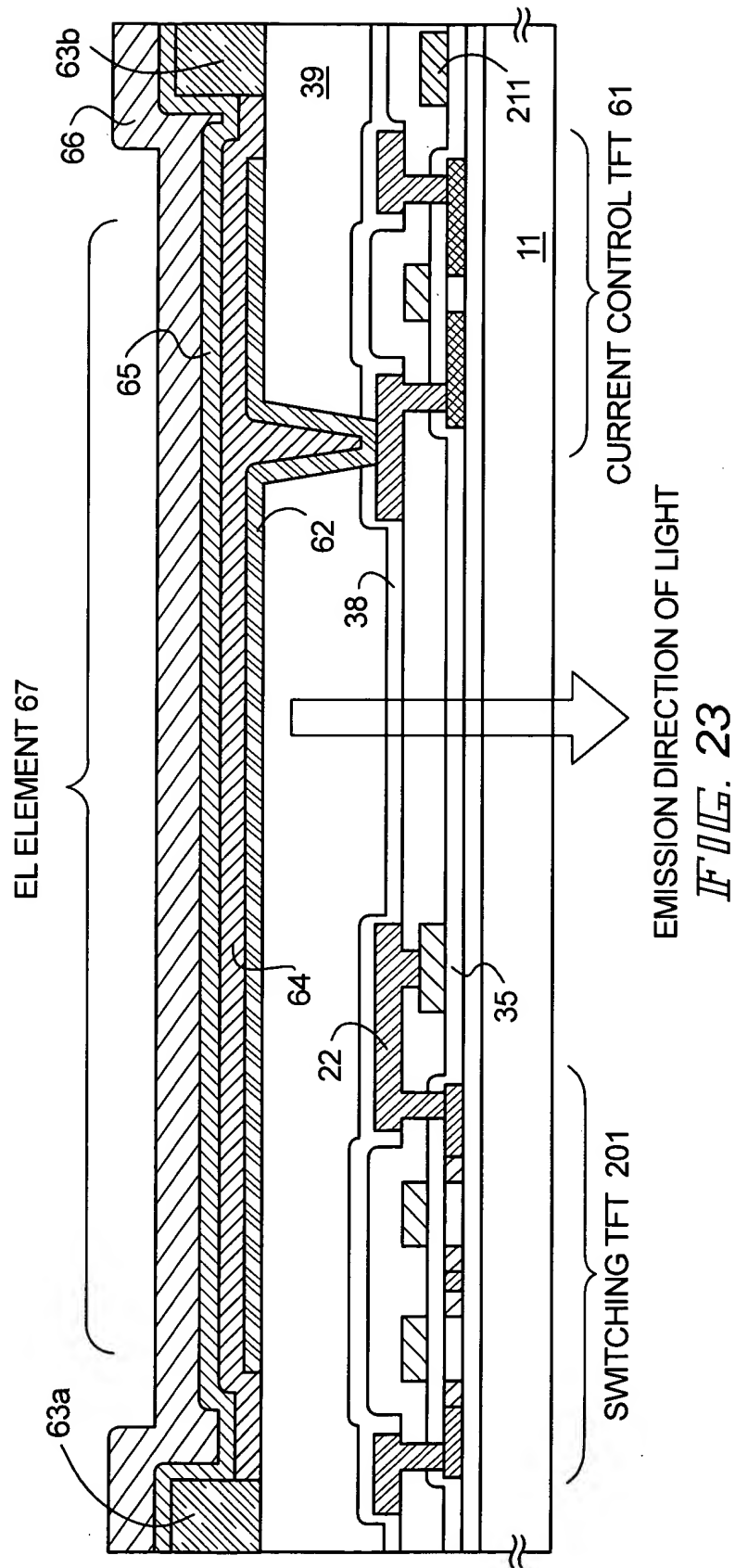


FIG. 22B



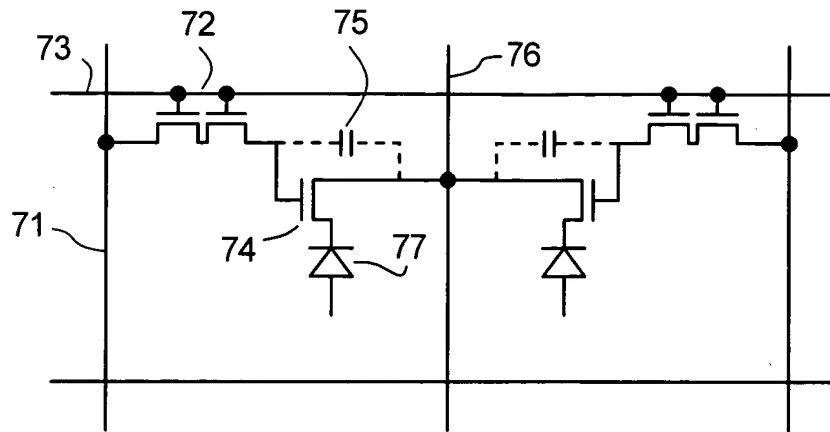


FIG. 24A

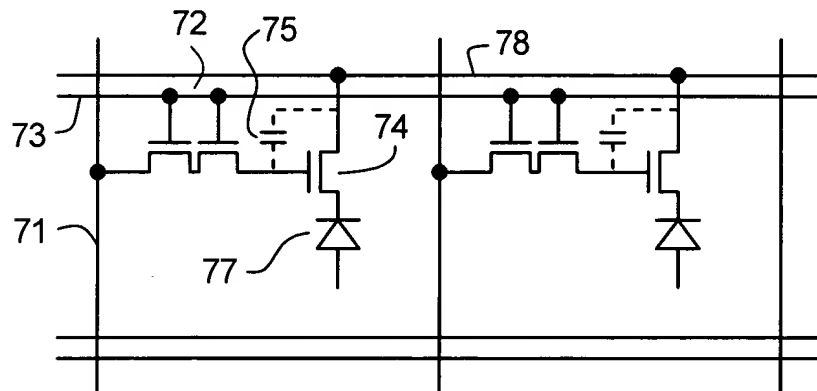


FIG. 24B

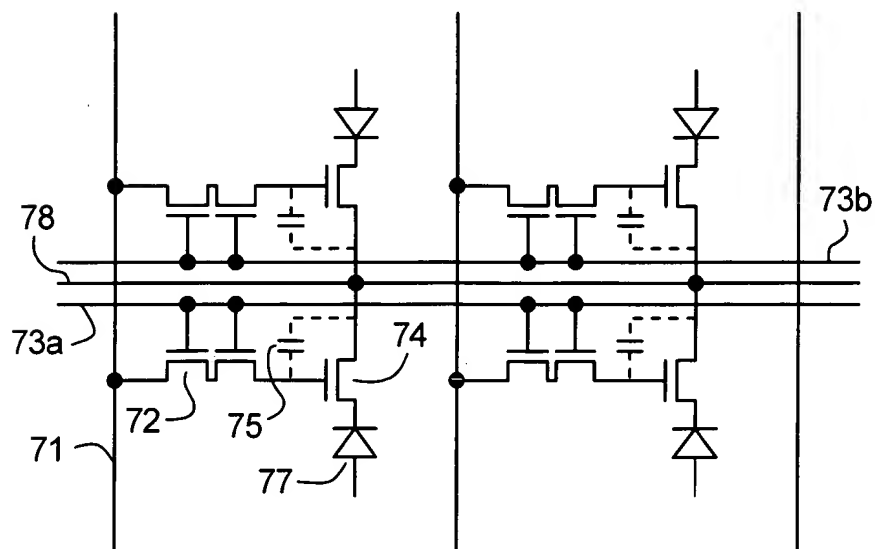


FIG. 24C

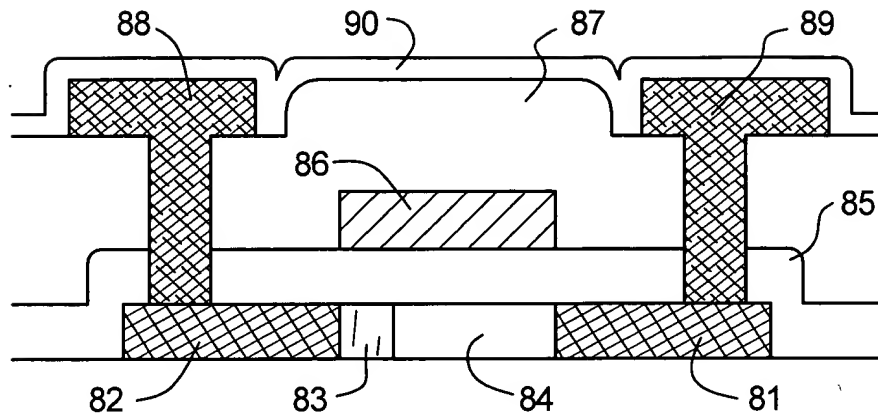


FIG. 25a

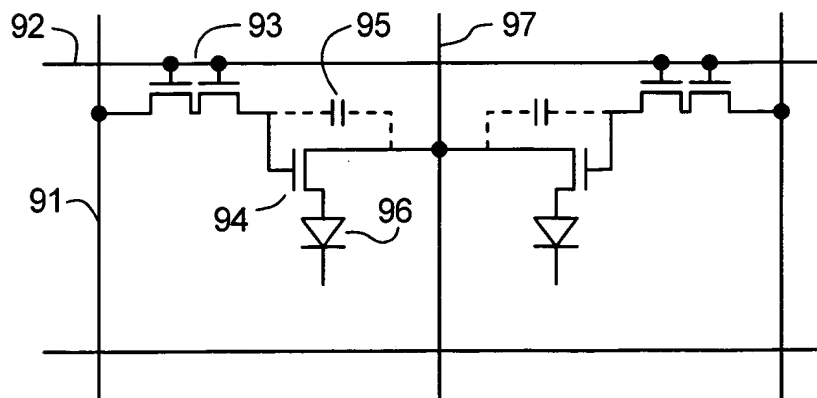


FIG. 25B